



microchip control processing network firewall f

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Patents 1 - 3 on **microchip control proce**

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[APPLICATION] **Global network computers**

US Pat. 9935779 - Filed Aug 24, 2001

10A: namely, a master **control** and/or **processing** unit 93 and one or more slave
... of the **microchip** 90 to establish or change its internal **firewall** 50 could

...

[APPLICATION] **Microchip-enabled online transaction system**

US Pat. 9952490 - Filed Sep 12, 2001

One skilled in the art will appreciate that a **network** may include any system for
exchanging ... **magnetic** storage devices, flash card memory and/or the like. ...

[APPLICATION] **Secure communication of information via a communication network**

US Pat. 9965164 - Filed Sep 27, 2001

Note, **network** includes a **firewall** 130, and **network** 125 includes a ... can be a
semiconductor-based microprocessor (in the form of a **microchip**), for example.

...

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firewall faraday OR cage OR magnetic OR shield**.

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Patents 1 - 10 on **processor surrounded**

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Faraday cage for a printed circuit card

US Pat. 5475606 - Filed Mar 5, 1993 - International Business Machines Corporation
 ... of the computer and a digital signal **processor** which processes the commands
 into ... and second ground planes, substantially **surrounded** by the **Faraday cage**.

Faraday cage for a printed circuit card

US Pat. 5592391 - Filed Jun 7, 1995 - International Business Machines Corporation
 Lastly, to the digital signal **processor** 233 incorporated into the audio ...
 active device are defined and are substantially **surrounded** by the **Faraday cage**,
 ...

Computer including a Faraday cage on printed circuit board

US Pat. 5587920 - Filed Jun 7, 1995 - International Business Machines Corporation
 This creates the top plate of the **Faraday cage** along with the component foot
 prints. At this state we have now 15 completely **surrounded** ...

Method and apparatus for dynamic magnetic field neutralization

US Pat. 4963789 - Filed May 1, 1989 - Contrac SCD, Inc.
 All of this assembly is enclosed within a **magnetic shield** 22. ... The entire
 electronic system is 35 controlled by a **processor** 32 which receives input data
 ...

Magnetic bubble memory cassette

US Pat. 4459680 - Filed Oct 28, 1981 - Fujitsu Limited
 2 and 3, 31 and 32 are molded upper IS serves also as a **magnetic shield** case,
 9, an case 32. area **surrounded** by solid lines 91 and 92 represents an As
 ...

Electromagnetic load detection device

US Pat. 6345544 - Filed Aug 26, 1999 - Yamaha Hatsudoki Kabushiki Kaisha
 12 is a block diagram showing the structure of the signal **processor** circuit of
 the ... The joint 5 acts as a shock absorber and a **magnetic shield** member. ...

Structure and method for mounting a circuit module

US Pat. 6205027 - Filed Mar 30, 1999 - Kabushiki Kaisha Toshiba
 Each of the plurality of third through holes is **surrounded** by a land portion
 provided on the base circuit board for securing a connection to the printed ...

Antenna device and a radio communication device including an antenna device

US Pat. 6342869 - Filed May 3, 2000 - Allgon A.B.
 Since the support structure 104 is substantially completely **surrounded** with a
 ... to a ground plane means 102, the space 114 constitutes a **Faraday cage**. ...

processor surrounded "faraday cage" OR "me

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Patents **1 - 20** on **multi core microproces**

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Local stall **control** method and structure in a **microprocessor**

US Pat. 6279100 - Filed Dec 3, 1998 - Sun Microsystems, Inc.

The pipeline **control unit** 226 also **processing unit** 110, copying objects or ...
closely- of register file segments 224 to form a **multi**-ported structure ...

Processor with multiple execution pipelines using pipe stage state ...

US Pat. 6138230 - Filed Jul 29, 1997 - VIA-Cyrix, Inc.

Microprocessor 1.2. System 2. Generalized Pipeline Flow 3. Pipeline Control 3.1.
Generalized Stall Control 3.2. Pipe Switching 3.3. **Multi**-box Instructions ...

Pipelined processor with register renaming hardware to accommodate multiple ...

US Pat. 5630149 - Filed Nov 20, 1995 - Cyrix Corporation

ISA-b illustrate operand forwarding; titans issued to it by the CPU **core** 20.

FIGS. 16a-b illustrate result forwarding- The **microprocessor** uses internal ...

Method and arrangement for data transmission between units on a bus system ...

US Pat. 6032178 - Filed Jan 12, 1998 - Siemens Aktiengesellschaft

The **microprocessor** described comprises a 30 central **processing unit (core**
processor) and various peripheral units (serial I/O, timer, DMA controller). ...

Pipelined processor with microcontrol of register translation hardware

US Pat. 6073231 - Filed Mar 17, 1997 - Via-Cyrix, Inc.

Microprocessor 1.2. System 2. Generalized Pipeline Flow 3. Pipeline Control 3.1.
Generalized Stall Control 3.2. Pipe Switching 3.3. **Multi**-box Instructions ...

Altering thread priorities in a multithreaded processor

US Pat. 6212544 - Filed Oct 23, 1997 - International Business Machines Corporation

Central **processing unit** (CPU) 100 for processing instruc- Instructions from ...

CPU 100 and main memory also com- **control unit** 200, and floating point unit ...

Method and apparatus for selecting thread switch events in a multithreaded ...

US Pat. 6697935 - Filed Oct 23, 1997 - International Business Machines Corporation

In a preferred embodiment, the processor **core** 100 of the data processing system

10 is a single integrated circuit, pipelined, superscalar **microprocessor**, ...

Universal paging device with power conservation

US Pat. 4518961 - Filed Jan 30, 1984 - Motorola, Inc.

A timer signal for actuation is supplied to a timer **control unit** 100 containing

a ... Also connected to the central **processing unit** are data directional ...

Air/fuel induction system for a **multi**-cylinder internal combustion engine

US Pat. 4617904 - Filed Nov 28, 1983 - Solex (U.K.) Limited

1 30 also includes an electronic **control unit** 10 which is generally similar



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Processor with multiple execution pipelines using pipe stage state ...

US Pat. 6138230 - Filed Jul 29, 1997 - VIA-Cyrix, Inc.

Exemplary **Processor** System 1.1. **Microprocessor** 1.2. System 2. ... Loads are controlled by the CPU **core** 20, and cacheable stores are directed through ...

Pipelined processor with register renaming hardware to accommodate multiple ...

US Pat. 5630149 - Filed Nov 20, 1995 - Cyrix Corporation

ISA-b illustrate operand forwarding; titans issued to it by the CPU **core** 20. ... of the **microprocessor** are omitted as each steße' me P1?6 **control unit** keeps ...

Altering thread priorities in a multithreaded processor

US Pat. 6212544 - Filed Oct 23, 1997 - International Business Machines Corporation

2 illustrates a means for enabling the processing means to switch process-
processor core 100. In a preferred embodiment, the proces- ing from a first ...

Pipelined processor with microcontrol of register translation hardware

US Pat. 6073231 - Filed Mar 17, 1997 - Via-Cyrix, Inc.

Exemplary **Processor** System 1.1. **Microprocessor** 1.2. System 2. ... Loads are controlled by the CPU **core** 20, and cacheable stores are directed 40 through ...

Method and apparatus for selecting thread switch events in a multithreaded ...

US Pat. 6697935 - Filed Oct 23, 1997 - International Business Machines Corporation

In a preferred embodiment, the **processor core** 100 of the data processing system 10 is a single integrated circuit, pipelined, superscalar **microprocessor**, ...

High performance integrated processor architecture including a sub-bus ...

US Pat. 5557757 - Filed Feb 2, 1994 - Advanced Micro Devices

Integrated **processor** 210 includes a CPU **core** 240 coupled to a memory controller 242, a PCI bus interface unit 244, an ISA sub-bus **control unit** 246, ...

Method and apparatus to force a thread switch in a multithreaded processor

US Pat. 6076157 - Filed Oct 23, 1997 - International Business Machines Corporation

Within the CPU 100, a **processor core** 110 contains specialized functional units, ... 1, the storage **control unit** 200 includes a transition cache 210 for ...

Local stall control method and structure in a microprocessor

US Pat. 6279100 - Filed Dec 3, 1998 - Sun Microsystems, Inc.

The pipeline **control unit** 226 also **processing unit** 110, copying objects or ... a pragmatic very large instruction word (VLIW) **core** of the **processor** 100. ...

Configuration and control unit for a heterogeneous multi-system

US Pat. 4075693 - Filed Dec 15, 1976 - International Business Machines Corporation

Any other non-volatile storage devices may likewise be used for each PSD, such



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Patents 1 - 17 on EMI shield "control unit"

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Data current coupler with internal shielding for electronic package

US Pat. 5128835 - Filed Aug 31, 1990 - AMP Incorporated

10 mation of an electromagnet **core**, a quick action panel CTCl r, ... and transmit signals rem ler from **EMI**/RFI without a separate **shield** along the data bus. ...

Local bus IDE architecture for a split computer system

US Pat. 5948092 - Filed Oct 7, 1997 - International Business Machines Corporation

An electromagnetic interference (**EMI**) **shield** is disposed between the cover 132 and chassis 130. The **processing unit** 18 includes other elements which are ...

System and method for controlling device which is present in media console ...

US Pat. 5898843 - Filed Oct 8, 1997 - International Business Machines Corporation

An electromagnetic interference (**EMI**) **shield** 160 is disposed between the cover 132 and chassis 130. 30 The **processing unit** 18 includes other elements which ...

Data current coupler

US Pat. 5105095 - Filed Jun 20, 1991 - AMP Incorporated

... to a **control unit** to sense and transmit signals along the data bus. ... of a data current coupler from **EMI**/RFI without a separate **shield** member. ...

Connector having a removable **EMI** filter

US Pat. 6517384 - Filed Jan 31, 2001 - Sony Computer Entertainment, Inc.

The sound system has an audio **processing unit** that plays background music ... the voltage impression wires to pierce ferrite **core** 83, **EMI** measures are taken ...

Platform spring

US Pat. 5226634 - Filed Aug 31, 1990 - AMP Incorporated

1 2 ("**EMI**") shielding using metallic plating on the upper PLATFORM SPRING member rather than a separate **shield** member, a rugged electromagnet biasing spring ...

Electromagnet insert for data current coupler

US Pat. 5192832 - Filed Aug 31, 1990 - AMP Incorporated

10 a electromagnet **core**, a quick action panel mounting ^,«™™, _ means, ... signals rent]er from **EMI**/RFI without a se arate **shield** along the data bus. ...

Integrated portable videoconferencing unit

US Pat. 6847403 - Filed Nov 5, 1997 - Polycom, Inc.

This is similar to the **control unit** used with room conferencing systems such as ... and the camera is mounted on camera post 19, **EMI** shielding 84 (FIG. ...

Mounting means

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Interface between a system **control unit** and a service **processing unit** of a ...

US Pat. 5146564 - Filed Feb 3, 1989 - Digital Equipment Corporation

A method for controlling the flow of data between a receive buffer of a service **processing unit** and a transmit buffer of a system **control unit** of a computer ...

Clock **control unit** responsive to a power management state for clocking ...

US Pat. 5586308 - Filed Oct 19, 1994 - Advanced Micro Devices, Inc.

2 is a block diagram of a clock **control unit** and associated input/output signals
... CPU **core** 14 is a data **processing unit** which implements a predetermined ...

Control apparatus for controlling data flow between a **control processing** ...

US Pat. 4103328 - Filed Apr 7, 1975 - Compagnie Internationale pour l'Informatique Cii-Honeywell Bull (Societe Anonyme)

Line DOLI transmits data bytes to interfaces DAI or PSI, the signals being transmitted from the **processing unit** 201 to the interface **control unit** 202. ...

Integrated circuit device having a **core** controller, a bus bridge, a ...

US Pat. 6292201 - Filed Nov 25, 1998 - Silicon Integrated Systems Corporation

The integrated circuit device as claimed in claim 1, wherein said **core** ... that includes a **processing unit**, a host bus connected to the **processing unit**, ...

Microprogrammed computer **control unit** capable of efficiently executing a ...

US Pat. 4179736 - Filed Nov 22, 1977 - Honeywell Information Systems Inc.

MICROPROGRAMMED COMPUTER **CONTROL UNIT** CAPABLE OF EFFICIENTLY EXECUTING A LARGE REPERTOIRE OF INSTRUCTIONS FOR A HIGH PERFORMANCE DATA **PROCESSING UNIT** 5 ...

User-removable central **processing unit** card for an electrical device

US Pat. 5748912 - Filed Jun 13, 1995 - Advanced Micro Devices, Inc.

The user-removable CPU card of claim 11 further comprising: a resource **control unit** included in the circuit and coupled to the central **processing unit**; ...

Network **control** system for dynamically switching a logical connection ...

US Pat. 5121486 - Filed Nov 18, 1988 - Hitachi, Ltd

7 is a flowchart showing the operation of a By the above system, switching from the replaced logical session establishing program; central **processing unit** ...

Test vector generator comprising a decompression **control unit** and a ...

US Pat. 5499248 - Filed Jan 31, 1994 - Hewlett-Packard Company

Assigned to the sequence controller is a decompression **control unit** which is ...
In general, the decompression **control unit** receives control signals, ...

Multiprocessor system including a transfer queue and an interrupt **processing** ...

US Pat. 5557744 - Filed Jul 28, 1993 - Fujitsu Limited



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Patents 1 - 20 on microprocessor "contr

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Central processing unit for use in a microprocessor

US Pat. 3969724 - Filed Apr 4, 1975 - The Warner & Swasey Company

1 2 memories outside the central **processing unit** reduce CENTRAL ... The **control unit** Within recent years the uses of data processing sys- of the CPU can ...

Logically transportable microprocessor interface control unit permitting bus ...

US Pat. 4727477 - Filed Mar 22, 1985 - International Business Machines Corp.

A single-chip **microprocessor** comprising: external signal pins to which off-chip devices are connected; a microprocessing unit; and an interface **control unit** ...

Microprocessor with two groups of internal buses

US Pat. 5379394 - Filed Jul 13, 1990 - Kabushiki Kaisha Toshiba

1 is a structural diagram of a conventional **microprocessor** or CPU (Central **Processing Unit**). A conventional CPU 10 is mainly composed of an arithmetic ...

Duplicate control and processing unit for telecommunications equipment

US Pat. 5784551 - Filed Mar 30, 1995 - Siemens Telecommunicazioni S.p.A.

T circuits towards the **processing unit**. SUMMARY OF THE INVENTION It is also known that a **microprocessor control unit** exerts To achieve said purposes the ...

Systems and apparatus for electronic communication and storage of time ...

US Pat. 5734891 - Filed Jun 12, 1996

The **microprocessor** con- clock 111 of the reader **control unit** control ... by a H8 /322 **microprocessor** central **processing unit** with the **control unit** CPU. ...

Two-way AC power line communications system

US Pat. 4429299 - Filed Jan 26, 1981 - Robertshaw Controls Company

Four bits of this data code pertain to data 15 previously provided to the room **control unit** from the central **processing unit**. The final four bits of the ...

Microprogram-coupled multiple-microprocessor module with 32-bit byte width ...

US Pat. 4736317 - Filed Jul 17, 1985 - Syracuse University

A microprogram-coupled multiple-**microprocessor** pointer **processing unit**, ... of said stack pointer **processing unit** in- 20 or rro eludes program **control unit** ...

Power-saving clock control apparatus and method

US Pat. 6073244 - Filed Aug 4, 1998 - Mitsubishi Denki Kabushiki Kaisha

This process also is applied to the peripheral **processing unit** 32 as well. ... The transfer request **control unit** 111 operates the clock **control unit** 112 so ...

Microprocessor control system utilizing overlapped programmable logic arrays

US Pat. 4661901 - Filed Dec 23, 1982 - International Business Machines Corporation

MICROPROCESSOR CONTROL SYSTEM Thus, it is desirable to find ways to increase the



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Duplicate **control** and **processing unit** for telecommunications equipment

US Pat. 5784551 - Filed Mar 30, 1995 - Siemens Telecommunicazioni S.p.A.

T circuits towards the **processing unit**. SUMMARY OF THE INVENTION It is also known that a **microprocessor control unit** exerts To achieve said purposes the ...

Apparatus for detecting updates to instructions which are within an ...

US Pat. 5742791 - Filed Feb 14, 1996 - Advanced Micro Devices, Inc.

For example, if **core** snoop **control unit** 50 asserts buffer 32 upon refetch ...
FIFO buffer 60 includes a plurality of storage pipeline of **microprocessor** 12.

...

Microprocessor with two groups of internal buses

US Pat. 5379394 - Filed Jul 13, 1990 - Kabushiki Kaisha Toshiba

1 is a structural diagram of a conventional **microprocessor** or CPU (Central **Processing Unit**). A conventional CPU 10 is mainly composed of an arithmetic ...

Central **processing unit** for use in a **microprocessor**

US Pat. 3969724 - Filed Apr 4, 1975 - The Warner & Swasey Company

1 2 memories outside the central **processing unit** reduce CENTRAL ... The **control unit** Within recent years the uses of data processing sys- of the CPU can ...

Microprocessor core power reduction by not reloading existing operands

US Pat. 6345363 - Filed Jun 23, 1998 - National Semiconductor Corporation

2 is a block diagram of a **microprocessor core** according to one embodiment of the present invention. FIG. 3 is a block diagram of the **control unit** in the ...

Microprocessor configured to translate instructions from one instruction set ...

US Pat. 6711667 - Filed Jun 28, 1996 - Legerity, Inc.

1, including a storage **control unit**. FIG. 3 is a block diagram of one embodiment of the storage ... CPU **core** 16 is a general purpose **microprocessor core**. ...

Systems and apparatus for electronic communication and storage of time ...

US Pat. 5734891 - Filed Jun 12, 1996

The **microprocessor** con- clock 111 of the reader **control unit** control ... by a H8 /322 **microprocessor** central **processing unit** with the **control unit** CPU. ...

Microprocessor having register bank architecture

US Pat. 5615348 - Filed Oct 14, 1994 - Kabushiki Kaisha Toshiba

4 is a configuration diagram of a conventional bank **control unit** ECU in the ...
6 is a general configuration diagram of a **microprocessor core** in a ...

Method for detecting updates to instructions which are within an instruction ...

US Pat. 6079217 - Filed Jan 29, 1998 - Advanced Micro Devices



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